



Attorney Docket No. 242/101

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

19/Ext 1.1
8/19/02
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AUG 13 2002
TECHNOLOGY CENTER 2800

In re Patent Application of

Ki-Young LEE, et al.

Examiner: Paul E. Brock II

Serial No. 09/389,491

Art Unit: 2815

Filed: September 3, 1999

For: SEMICONDUCTOR INTEGRATED CIRCUIT CAPACITOR AND METHOD
FOR FABRICATING SAME

PETITION FOR EXTENSION OF TIME
UNDER 37 C.F.R. § 1.136

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

It is respectfully requested that an extension of time for the period indicated below be granted in accordance with the provisions of 37 C.F.R. § 1.136 to take the action required in the application identified in caption, as reflected by the papers submitted herewith.

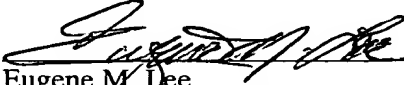
<u>X</u> First Month	\$110 (\$ 55)*
_ Second Month	\$290 (\$145)*
_ Third Month	\$520 (\$260)*
_ Fourth Month	\$520 (\$260)*
_ Fifth Month	\$500 (\$250)*

*(Small Entity) TOTAL FEE: \$110.00

The Commissioner is hereby authorized to charge the necessary extension fees to our Deposit Account No. 5-01645.

Respectfully submitted,

August 9, 2002
Date


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